

JNTUH COLLEGE OF ENGINEERING: HYDERABAD
(AUTONOMOUS)

B.Tech. III year II Semester, Supplementary Examinations,
APRIL, 2016

08' Batch

EXAMINATION TIME – TABLE

Exam Time: 02.00PM to 05.00 PM

Date / Day	ECE
22-04-2016	Digital Signal Processing
26-04-2016	VLSI Design
28-04-2016	Microwave Engineering
30-04-2016	Microprocessor & Interfacing

Date:07-04-2016

PRINCIPAL